

### Features

- Low insertion loss.
- Good rejection.
- LTCC Construction.
- temperature stable.
- Small size.

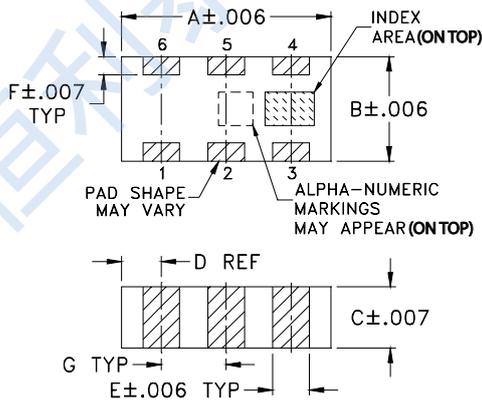
### Applications

- Sub-harmonic rejection.
- Transmitters/receivers.
- Lab use.

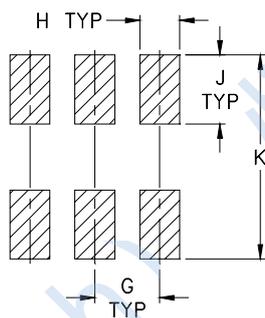
### Pad Connections

RF IN	1
RF OUT	3
GROUND	2,4,5,6

### Outline Drawing



### PCB Land Pattern

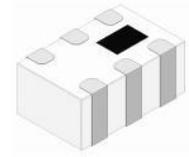


Suggested Layout,  
Tolerance to be within ±.002

### Outline Dimensions : inch mm

A	B	C	D	E	F
.126	.063	.035	.024	.022	.011
3.20	1.60	0.89	0.61	0.56	0.28
G	H	J	K		wt
.039	.024	.042	.123		grams
0.99	0.61	1.07	3.12		.020

### Functional Schematic



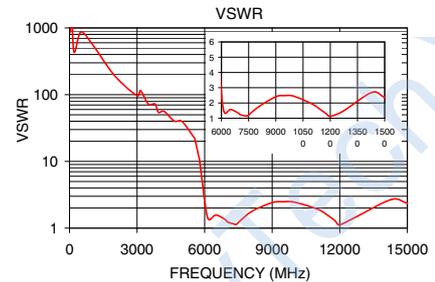
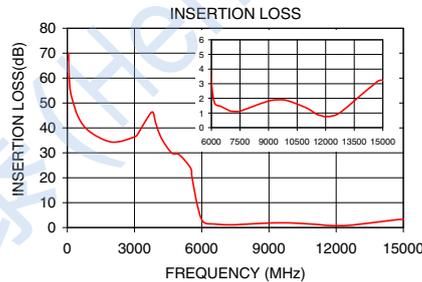
50 Ω  
6300MHz to 15000MHz

### Electrical Specifications(1) at 25°C

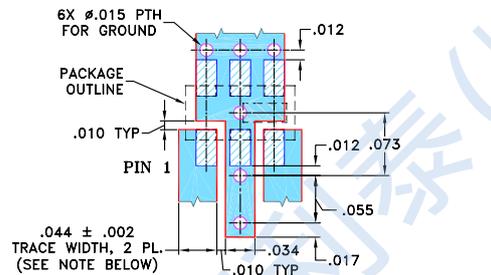
Parameter		Frequency (MHz)	Min.	Typ.	Max.	Units
Pass Band	Insertion Loss	6300-15000	—	—	5.0	dB
		6350-13000	—	—	3.0	
	VSWR	6050-8000	—	1.5	—	: 1
Stop Band	Rejection Loss	5190	—	30	—	dB
		5200	20	—	—	
	Freq. Cut-Off	6010	—	3.0	—	dB
	VSWR	5190-5200	—	20	—	: 1

1. In Application where DC voltage is present at either input or output ports, coupling capacitors are required.

### Typical Performance Data at 25°C



### Suggested PCB Layout



NOTE: 1. TRACE WIDTH IS SHOWN FOR ROGERS R04350 WITH DIELECTRIC THICKNESS: .020 ± .0015; COPPER: 1/2 OZ. EACH SIDE. FOR OTHER MATERIALS TRACE WIDTH MAY NEED TO BE MODIFIED.  
2. BOTTOM SIDE OF THE PCB IS CONTINUOUS GROUND PLANE.

- DENOTES PCB COPPER LAYOUT
- DENOTES COPPER LAND PATTERN FREE OF SOLDERMASK

### Maximum Ratings

Operating Temperature	-55°C to +125°C
Storage Temperature	-55°C to +125°C
RF Power Input*	7W at 25°C

\*Permanent damage may occur if any of these limits are exceeded.