

Features

- Flat group delay over passband, (0.6 ns typical)
- High rejection 40 dB
- Miniature shielded package
- Aqueous washable

Applications

- Test equipments
- Receivers / transmitters
- Harmonic rejection
- Military

HT-SXBP-202+



50Ω 198 to 206 MHz

Electrical Specifications at 25°C

Parameter		F#	Frequency(MHz)	Min.	Typ.	Max.	Unit
Pass Band	Center Frequency	-	-	-	202-	-	MHz
	Insertion Loss	F1-F2	198-206	-	3.4	5	dB
	VSWR	F1-F2	198-206	-	1.9	2.3	:1
Stop Band, Lower	Insertion Loss	DC-F3	DC-160	20-	32	-	dB
	VSWR	DC-F3	DC-160	-	33	-	:1
Stop Band, Upper	Insertion Loss	F4-F5	250-2700	20	31	-	dB
	VSWR	F4-F5	250-2700	-	27	-	:1

Maximum Ratings

Operating Temperature -40°C to 85°C

Storage Temperature -55°C to 100°C

RF Power Input* 0.4W max.

Permanent damage may occur if any of these limits are exceeded.

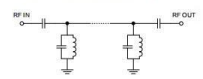
Typical Performance Data at 25°C

Frequency (MHz)	Insertion Loss (dB)	VSWR (:1)	Frequency (MHz)	Group Delay (nsec)
1	75.13	59.91	198.0	23.00
75	59.39	56.04	198.5	22.82
160	32.09	34.75	199.0	22.76
171	16.22	12.09	199.5	22.71
176	8.14	4.38	200.0	22.67
180	4.73	2.37	200.5	22.66
198	3.16	1.62	201.0	22.63
202	3.18	1.67	201.75	22.60
206	3.33	1.61	202.00	22.57
216	6.29	2.41	202.25	22.55
223	12.50	6.21	202.75	22.60
236	24.29	17.39	203.25	22.60
250	33.82	29.96	203.50	22.62
300	56.65	72.39	203.75	22.69
500	71.14	133.63	204.00	22.74
1000	74.29	96.51	204.50	22.83
1500	64.70	62.05	204.75	22.84
2000	50.37	57.91	205.00	22.86
2500	38.18	48.26	205.50	22.92
2700	34.48	46.96	206.00	23.03

Typical Frequency Response



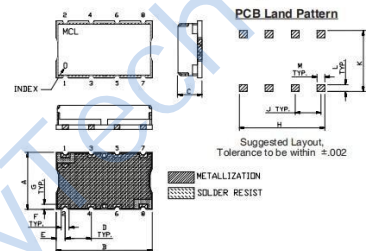
Functional Schematic



Pad Connections

INPUT	1
OUTPUT	8
GROUND	2,3,4,5,6,7

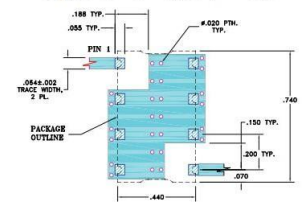
Outline Drawing



Outline Dimensions: Unit (mm)

A	11.18	D	5.08	G	1.02
B	18.80	E	1.78	H	16.76
C	6.86	F	1.52	J	5.08
L	1.40	M	1.52	K	11.94
wt	3.0				

Demo Board MCL P/N: TB-368 Suggested PCB Layout (PL-230)



- NOTE:
1. TRACE WIDTH IS SHOWN FOR FR4 WITH DIELECTRIC THICKNESS: .005" (125μm) COPPER: 1/2 OZ. EACH SIDE. FOR OTHER MATERIALS TRACE WIDTH MAY NEED TO BE MODIFIED.
 2. BOTTOM SIDE OF THE PCB IS CONTINUOUS GROUND PLANE.
- DENOTES PCB COPPER LAYOUT WITH SMDSC (SOLDER MASK OVER BARE COPPER)
 - DENOTES COPPER LAND PATTERN FREE OF SOLDER MASK

