

Low Pass Filter

Features

- high rejection
- sharp cut-off
- shielded package
- aqueous washable
- low cost

Applications

- test equipments
- defense communications
- receivers / transmitters
- harmonic rejection

HT-SXLP-95+

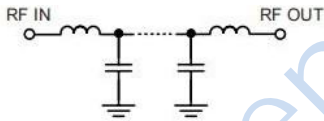


50Ω DC to 95 MHz

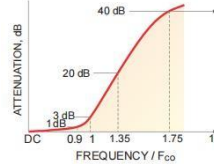
Low Pass Filter Electrical Specifications (T_{AMB}= 25°C)

PASSBAND (MHz)	FCO _s (MHz) Nom.	STOPBAND (MHz)		VSWR(:1)	
		(Loss < 20dB)	(Loss > 40dB)	Passband Typ.	Stopband Typ.
DC-95	108	146-189	189-1400	1.7	18

Functional Schematic



Typical Frequency Response



Typical Performance Data at 25°C

Frequency (MHz)	Insertion Loss (dB)		Return Loss (dB)	Frequency (MHz)	Group Delay (nsec)
	\bar{x}	σ			
0.5	0.02	0.01	52.90	0.5	8.17
1.0	0.04	0.00	49.11	10.0	8.06
10.0	0.08	0.00	41.48	20.0	8.08
30.0	0.15	0.01	29.77	30.0	8.16
70.0	0.31	0.01	24.07	40.0	8.37
95.0	0.53	0.01	23.82	50.0	8.68
100.0	0.79	0.03	18.19	60.0	9.14
104.0	1.57	0.10	8.88	70.0	9.80
108.0	3.30	0.21	4.43	80.0	10.87
115.0	8.14	0.47	1.48	85.0	11.76
120.0	12.00	0.61	0.84	90.0	12.95
146.0	28.80	0.94	0.31	91.0	13.19
189.0	48.66	1.53	0.20	92.0	13.44
200.0	53.38	2.00	0.18	93.0	13.66
400	72.49	2.86	0.07	94.0	13.88
800.0	80.12	3.70	0.15	95.0	14.08
1200.0	78.78	5.48	0.23	100.0	14.75
1400.0	71.99	2.28	0.26	108.0	14.53

Maximum Ratings

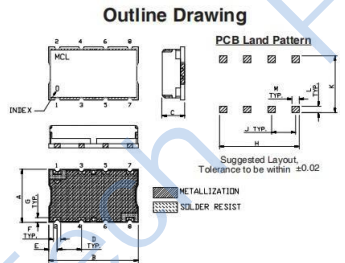
Operating Temperature	-40°C to 85°C
Storage Temperature	-55°C to 100°C
RF Power Input*	0.5W max.

Permanent damage may occur if any of these limits are exceeded.

Pin Connections

INPUT	1
OUTPUT	8
GROUND	2,3,4,5,6,7

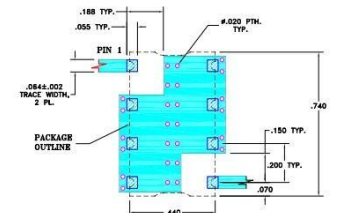
Outline Drawing



Outline Dimensions: Unit (mm)

A	11.18	G	1.02
B	18.80	H	16.76
C	6.86	J	5.08
D	5.08	K	11.94
E	1.78	L	1.40
F	1.52	M	1.52
WT	0.3g		

Demo Board P/N: TB-368
Suggested PCB Layout (PL-230)



NOTE:

1. TRACE WIDTH IS SHOWN FOR FR4 WITH DIELECTRIC THICKNESS: 0.025" (0.635mm) COPPER: 1/2 OZ. EACH SIDE. FOR OTHER MATERIALS TRACE WIDTH MAY NEED TO BE MODIFIED.
 2. BOTTOM SIDE OF THE PCB IS CONTINUOUS GROUND PLANE.
- DENOTES PCB COPPER LAYOUT WITH SMOBC (SOLDER MASK OVER BASE COPPER)
- DENOTES COPPER LAND PATTERN FREE OF SOLDER MASK