



H3-WBC1-1TL

0.25-750MHz 50Ω 1:1Ratio Balun

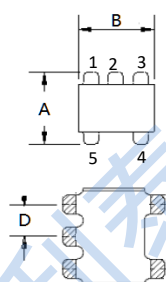
APPLICATIONS

Ideally suited for high volume cellular and wireless applications.

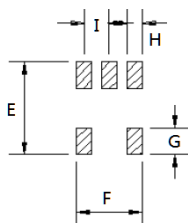
FEATURES

- Surface mount
- Impedance ratio: 1:1
- Available on tape and reel
- RoHS compliant and Pb free
- 260°C reflow compatible
- CT on Secondary

DIMENSIONS



Land Pattern



A	B	C
3.81	3.81	2.90 Max
D		
1.27		

E	F	G
4.83	3.30	1.40
H	J	
0.76	1.27	

Unless otherwise stated dimensions are in mm

Tolerance: .xx ± 0.25, .xxx ± 0.05

ELECTRICAL SPECIFICATIONS@25°C:

Parameters (P#: ENA PORT)	frequency band	Min.	Typ.	Max.	Unit
Port Impedance, Z0 (P1 / P2 / P3)			50/25/25		Ohm
Insertion Loss	1-100MHz		0.5	1.0	dB
	0.5-300MHz		1.1	2.0	dB
	0.25-750MHz		2.2	3.0	dB
Return Loss	1-100MHz	15.0	18.0		dB
	0.5-300MHz	7.0	10.0		dB
	0.25-750MHz	5.0	7.0		dB
Amplitude Balance	1-100MHz		0.1	±0.5	dB
	0.5-300MHz		0.3	±1.0	dB
	0.25-750MHz		0.9	±1.5	dB
Phase Balance (Nominal 180°)	1-100MHz		1.0	±5	Deg
	0.5-300MHz		2.0	±7	Deg
	0.25-750MHz		3.0	±10	Deg
CMRR	1-100MHz	30	45		dB
	0.5-300MHz	25	34		dB
	0.25-750MHz	20	26		dB

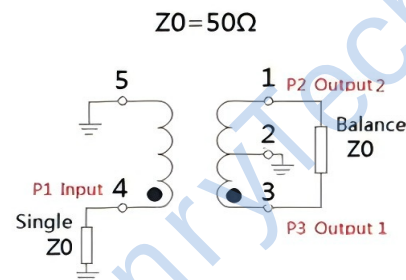
FUNCTIONAL SCHEMATIC



PIN CONFIGURATION

Pin No.	Function
1	Secondary Dot
2	Secondary CT
3	Secondary
4	Primary
5	Primary Dot

APPLICATION & TEST CIRCUIT



ABSOLUTE MAXIMUM RATINGS

Parameter	Value
Input Power	250mW MAX
DC Current	30mA MAX
Operating Temperature Range	-40 to 85°C

Tel: +86-28-85589664

E-mail: mr_li@henglitaitech.com

Web: www.hltchips.com

Address: Room 1401, 14th Floor, Building 5, Huaxi Science and Technology Innovation Center, No. 50, Shuxi Road, Jinniu District, Chengdu City



TEST CURVE

